

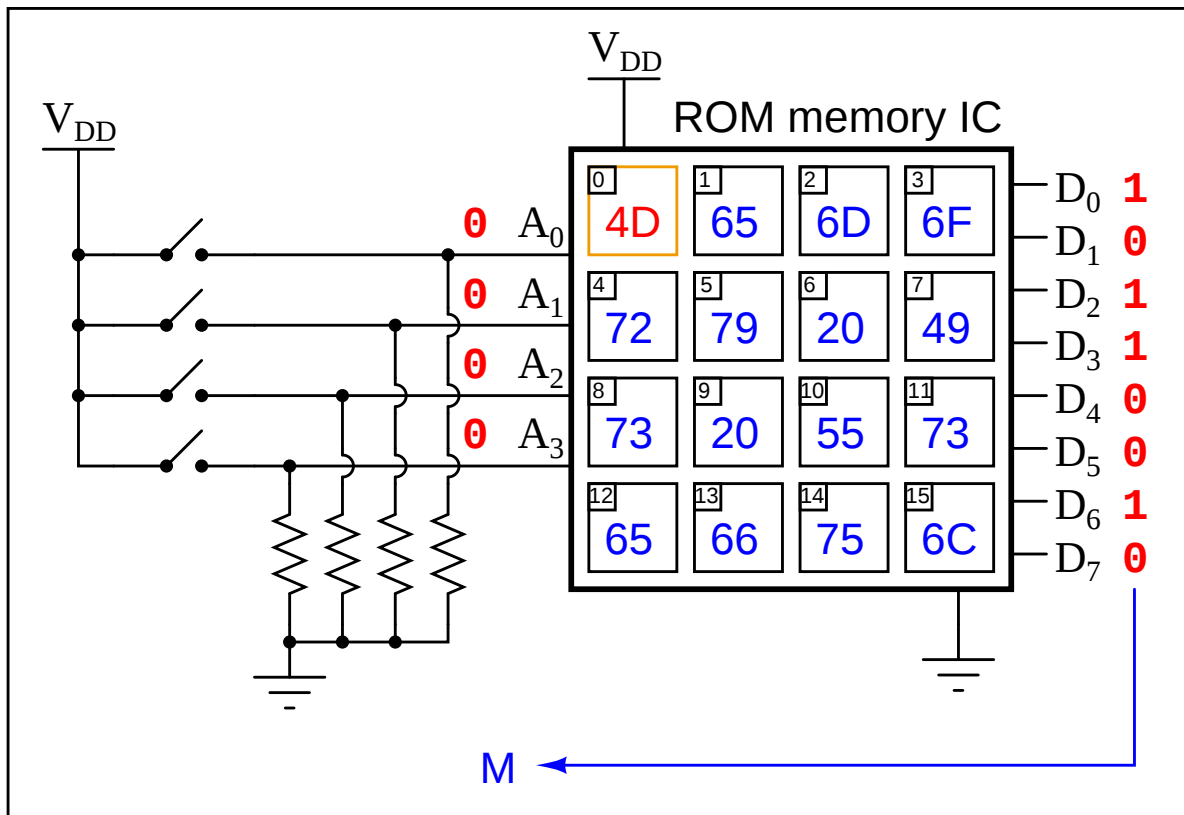
Question 1

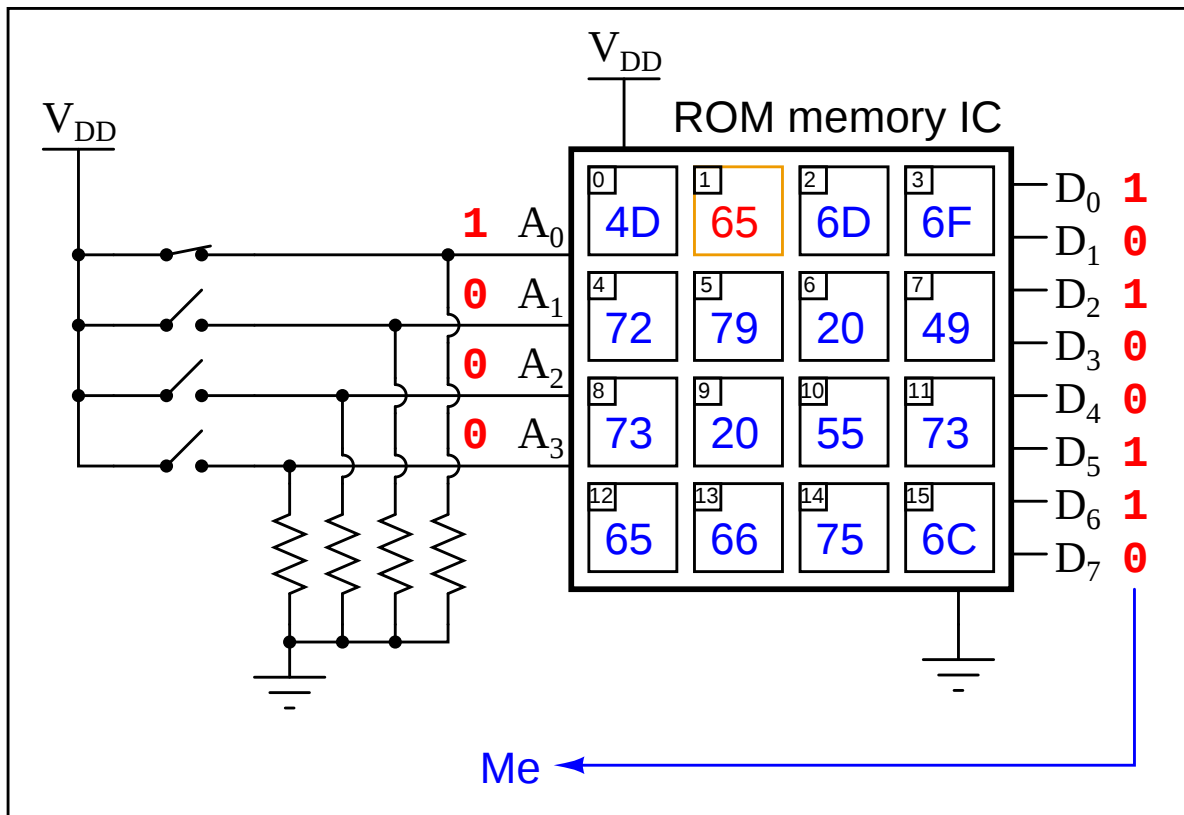
Animation: addressing 16×8 bit ROM memory

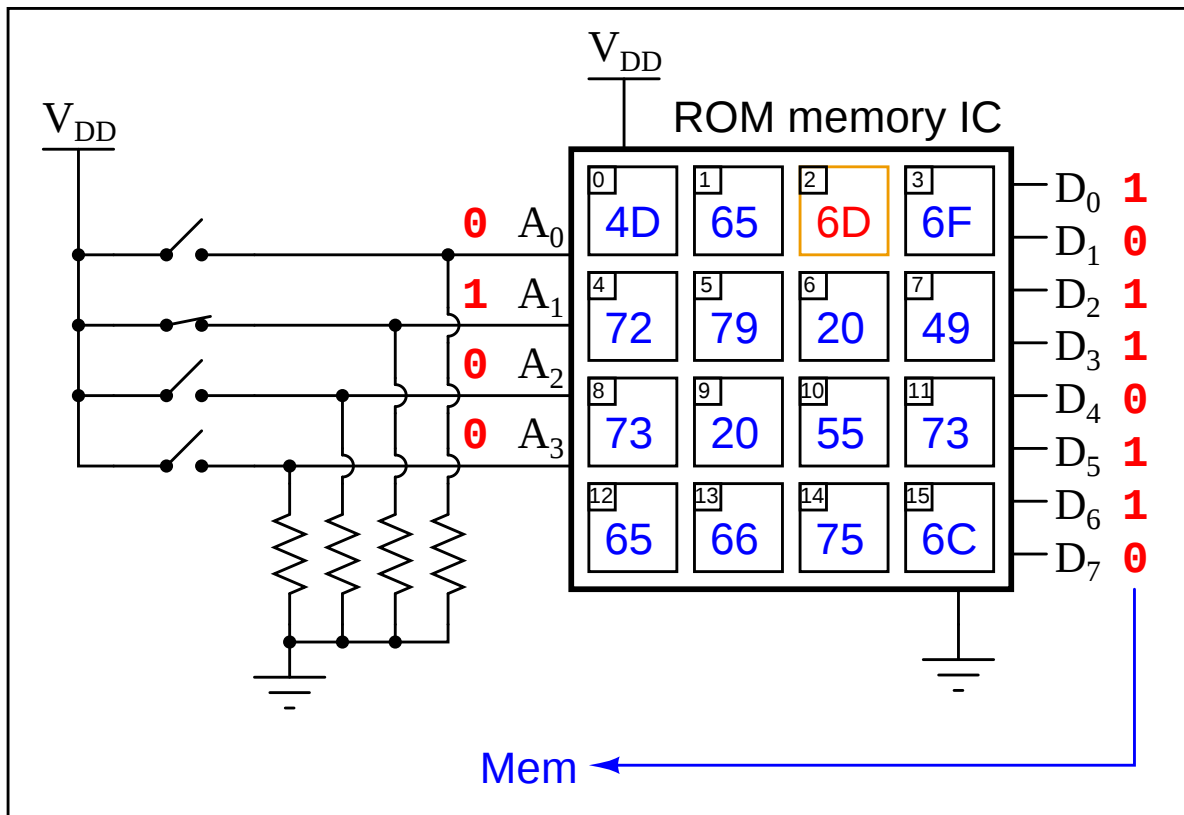
This question consists of a series of images (one per page) that form an animation. Flip the pages with your fingers to view this animation (or click on the "next" button on your viewer) frame-by-frame.

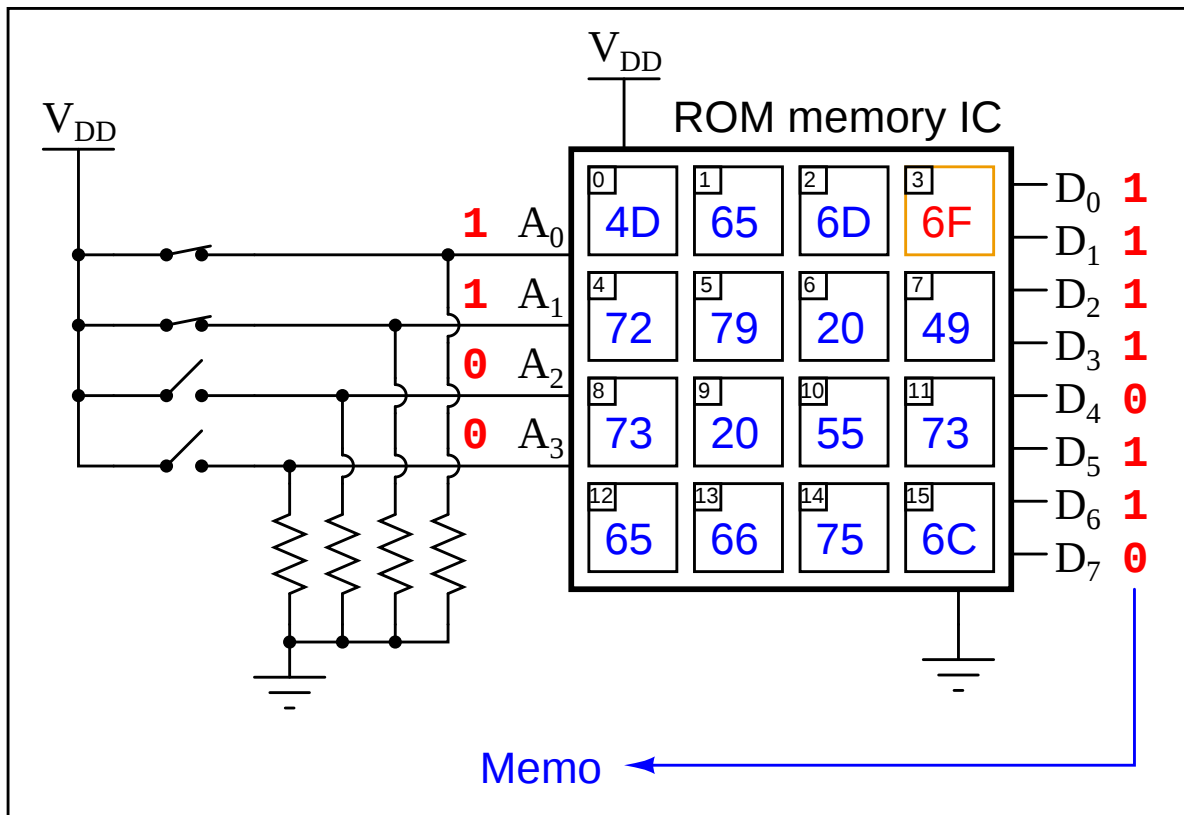
The following animation shows how setting the input (address) switches in particular combinations selects individual memory "cells" inside the ROM, resulting in data stored within those cells to appear at the output (data) lines. Questions to ponder:

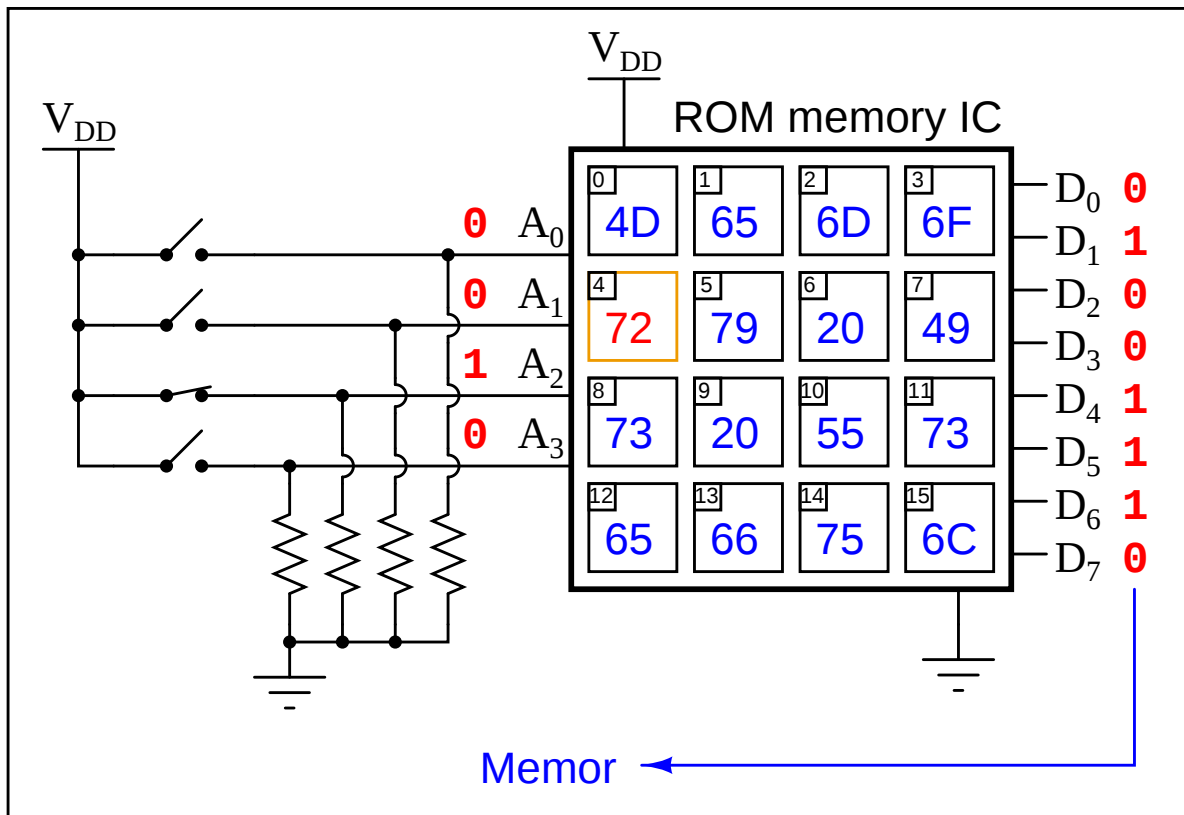
- What is the organization of this particular ROM chip? (e.g. 256×4 , $1k \times 1$, etc.)
- What is the relationship between the hexadecimal numbers stored inside each cell and the alphabetical characters shown below the chip? What code is being used to represent these characters?
- What character is represented by the hexadecimal code 20? (Hint: this code is used twice in the sequence shown.)

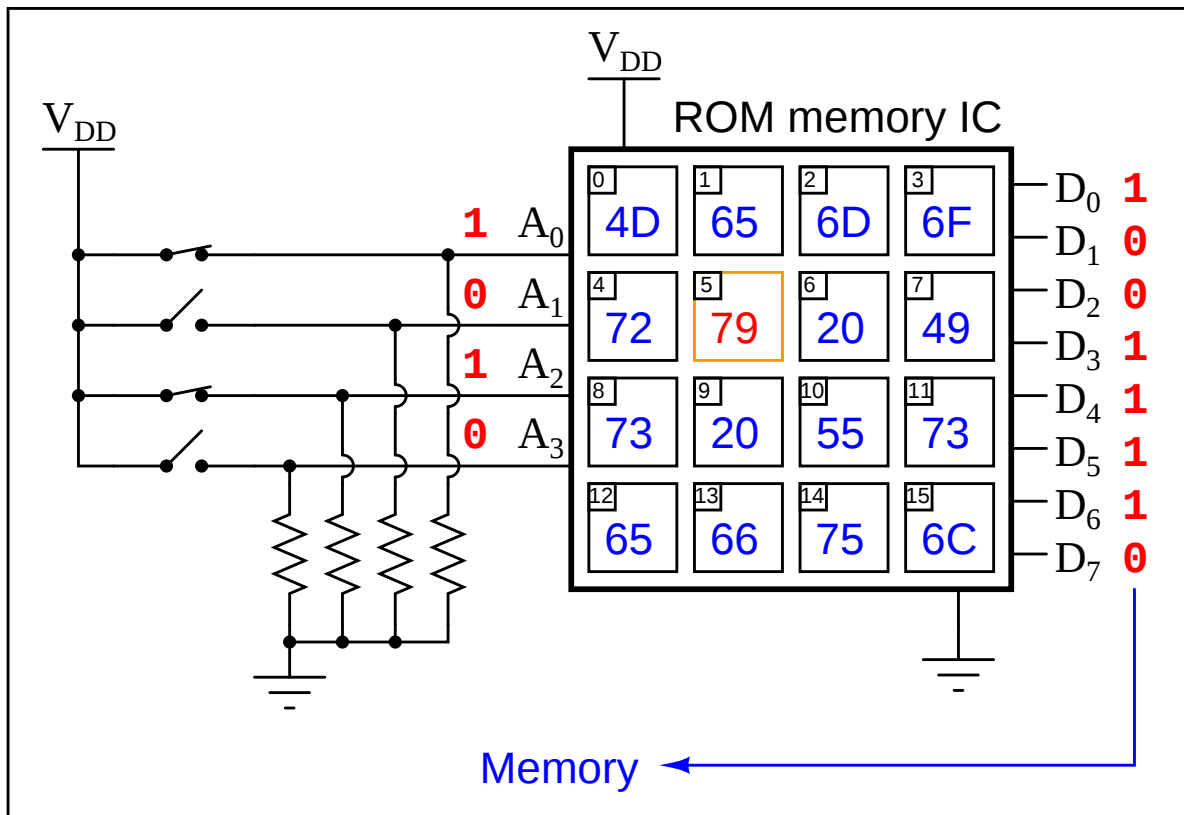


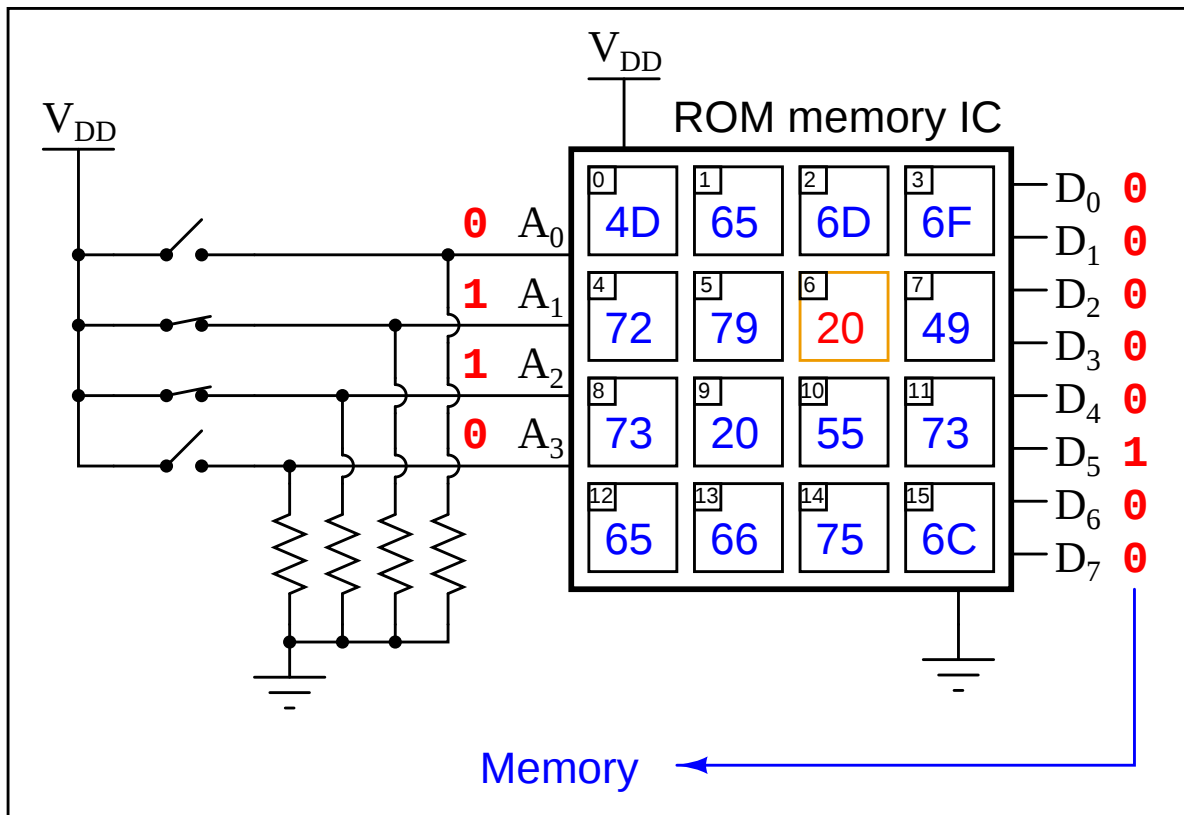


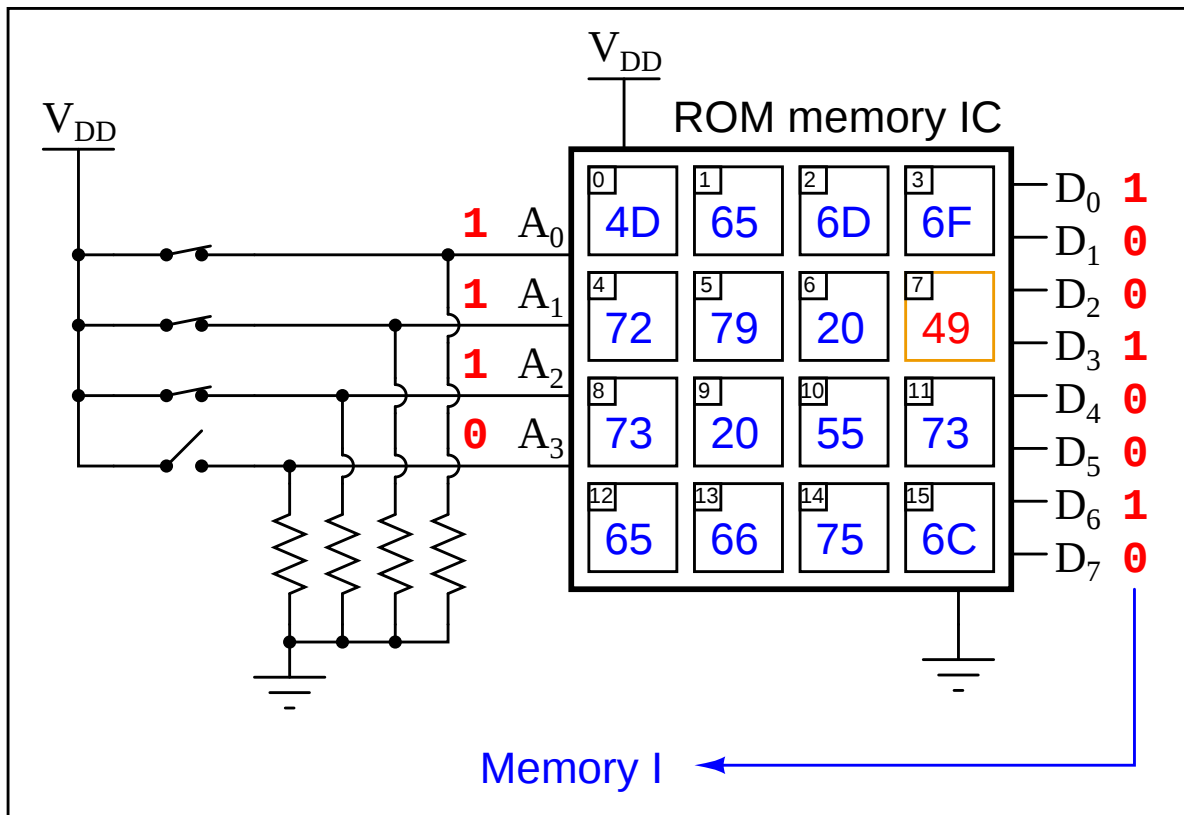


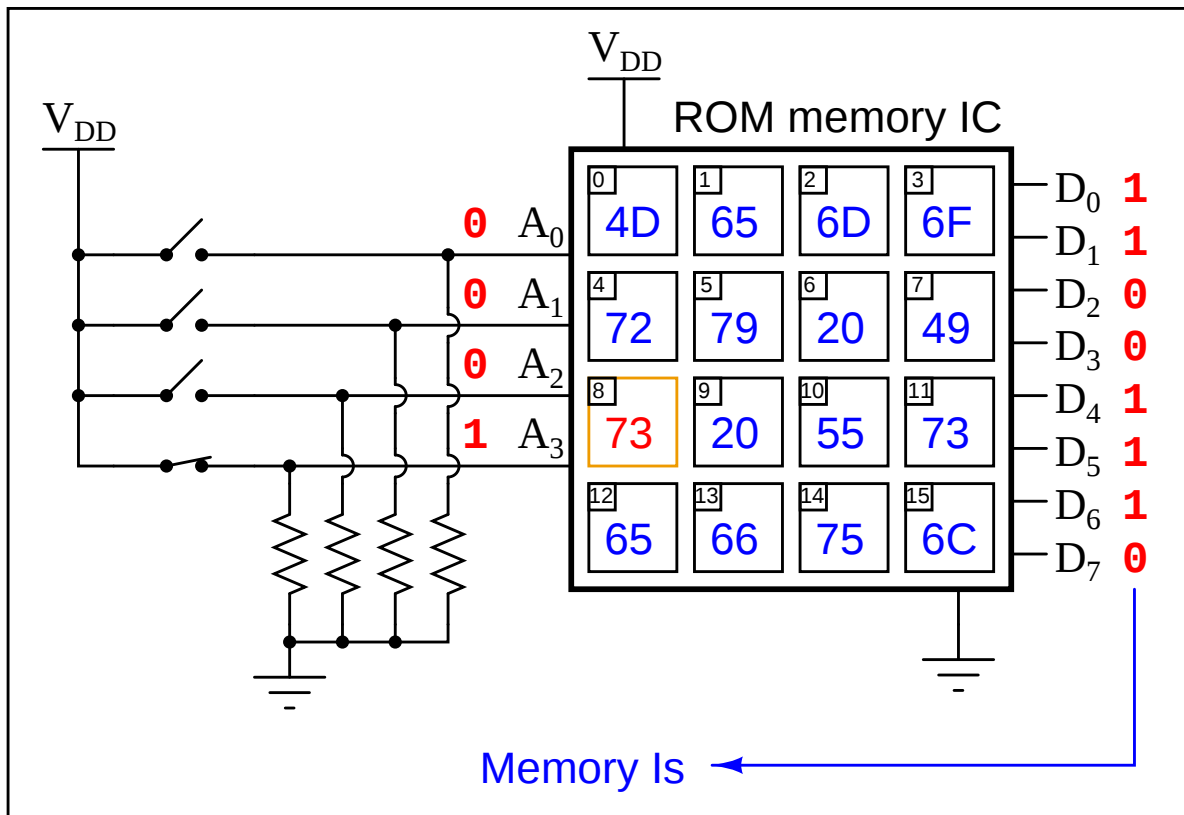


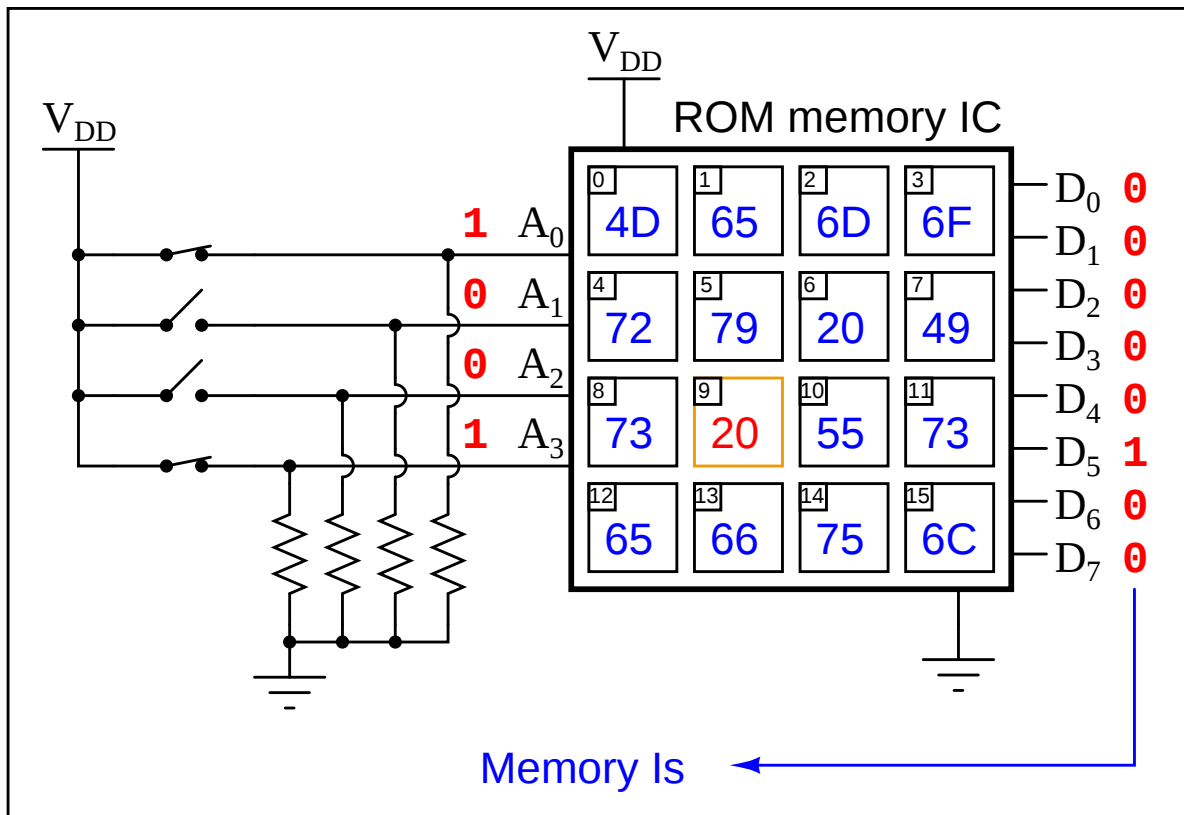


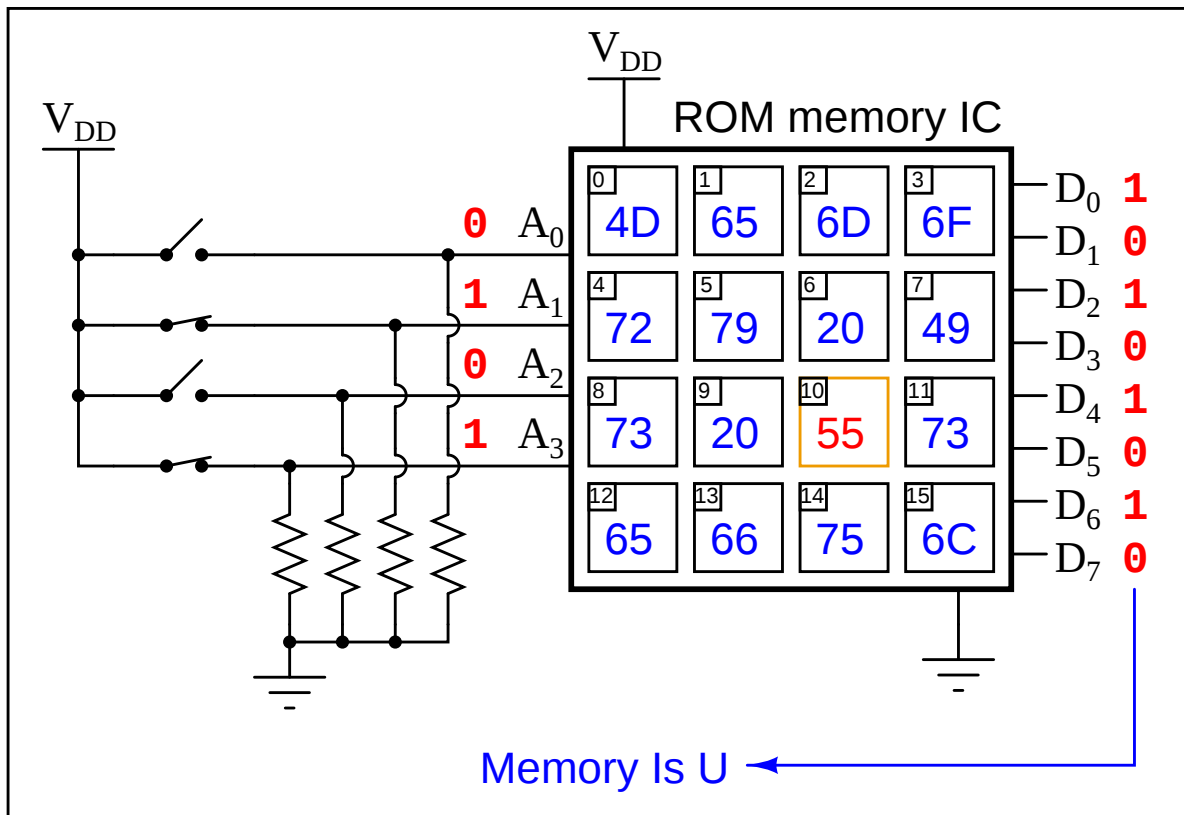


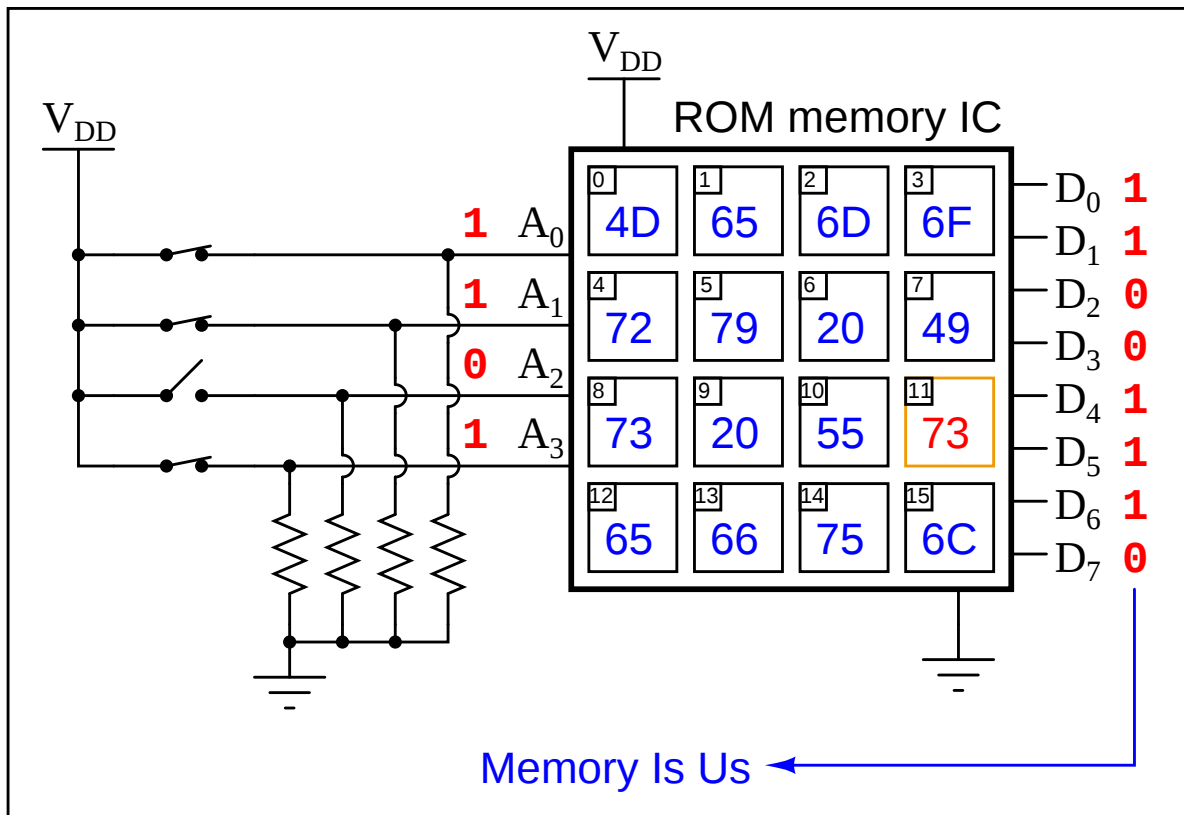


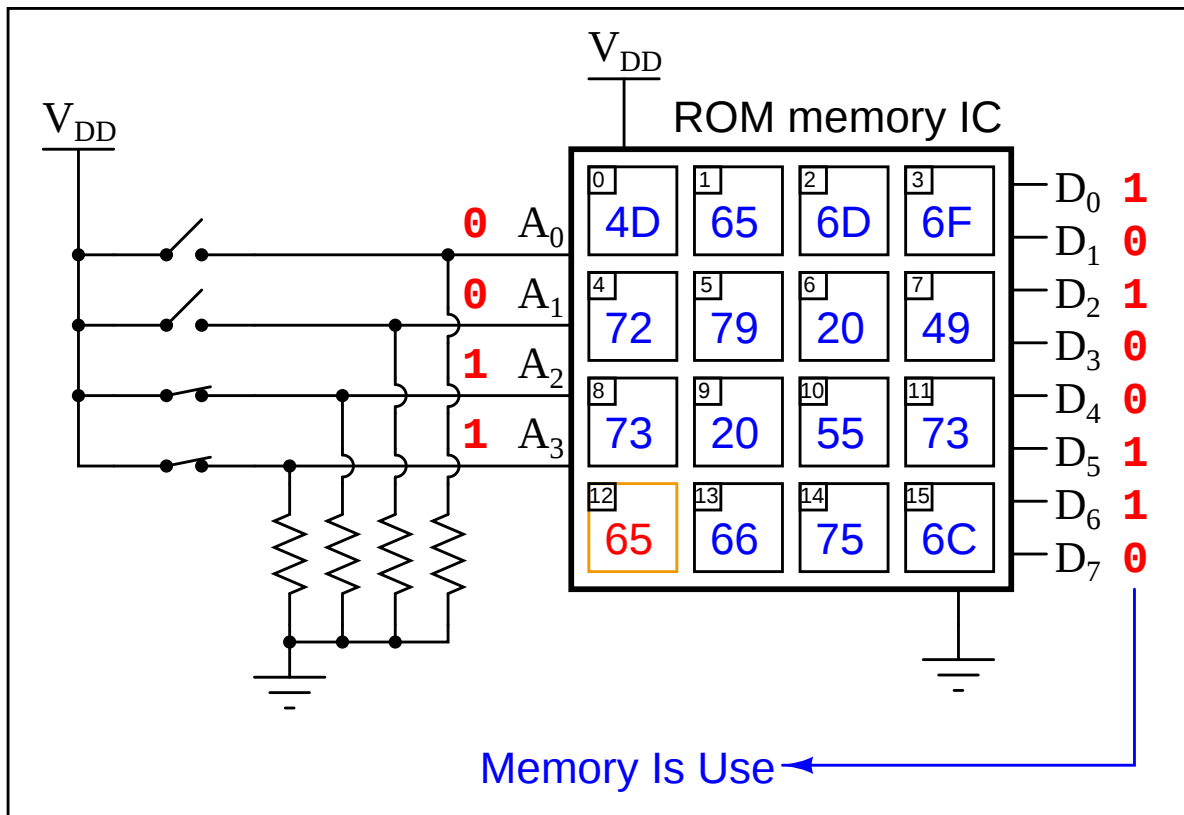


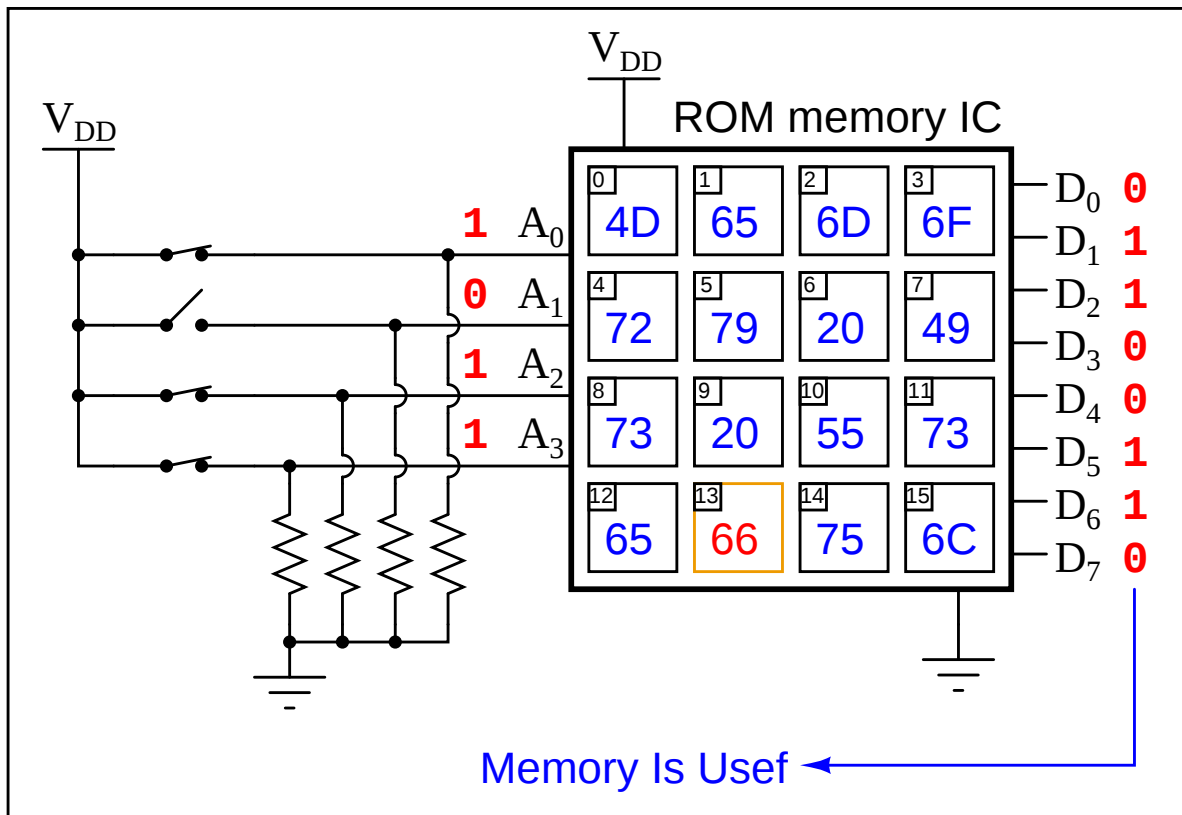


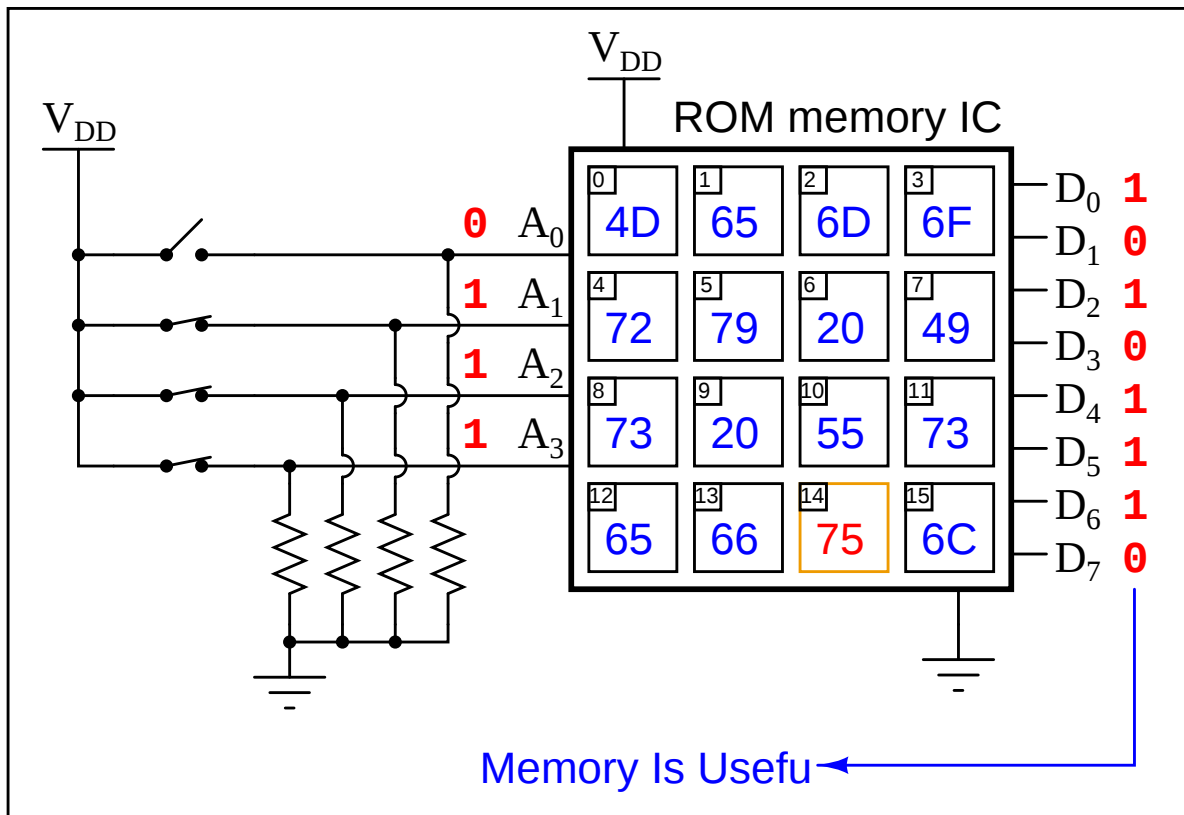


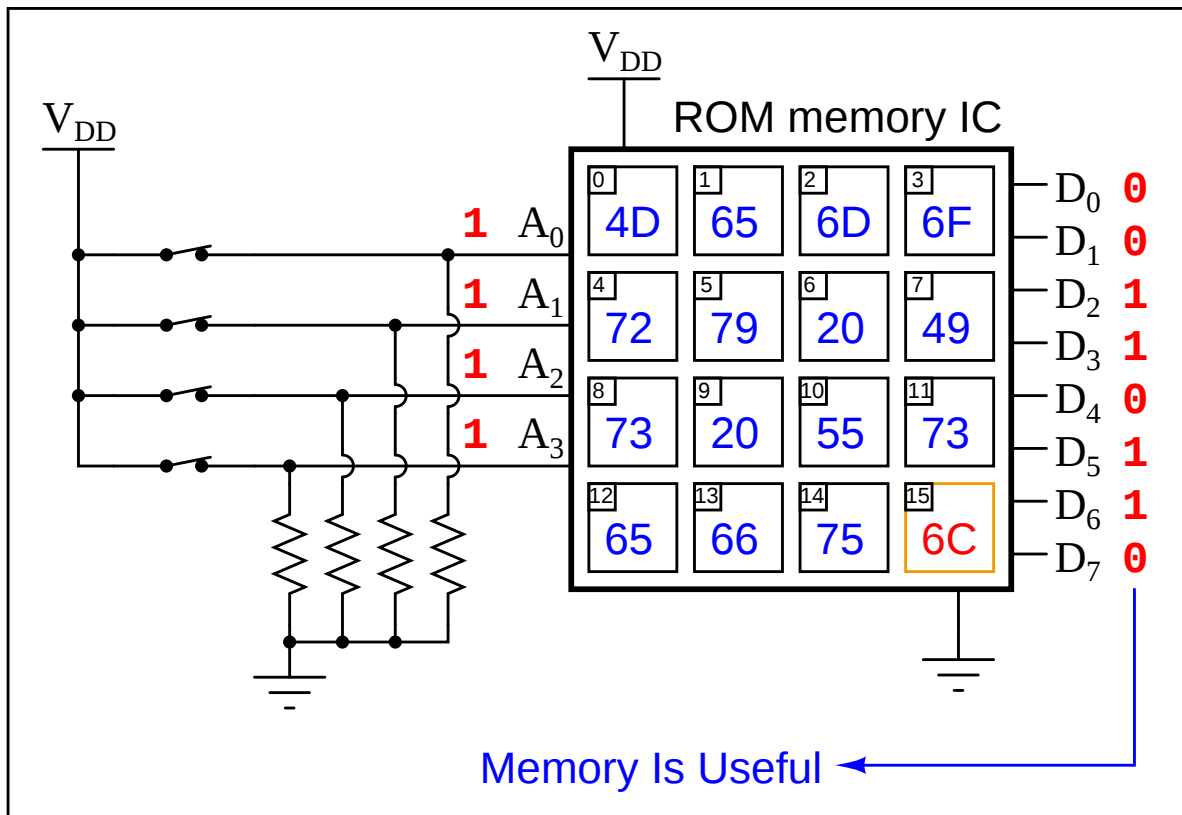












file 03244

Answer 1

Nothing to note here!

Notes 1

The purpose of this animation is to let students study the behavior of this switch circuit and reach their own conclusions. Similar to experimentation in the lab, except that here all the data collection is done visually rather than through the use of test equipment, and the students are able to "see" things that are invisible in real life.